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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of)	Group: 1107
MICHIO ASAHINA)	Examiner: H. Tsai
Serial No.: 07/780,455)	TRANSMITTAL OF ISSUE
Filed: October 22, 1991)	FEE AND FORMAL DRAWINGS
For: METHOD FOR FABRICATING A)	Date of NOA: 4/16/93
SEMICONDUCTOR DEVICE HAVING A)	Batch No.: M27
CONDUCTOR STRUCTURE WITH A PLATED)	
LAYER (AS AMENDED))	

Hon. Commissioner of Patents
and Trademarks
Washington, D.C. 20231

Dear Sir:

Enclosed herewith is a complete set (10 sheets) of formal drawings (Figs. 1-12) for the above-identified application to comply with the Notice of Drawing Requirements dated April 16, 1994.

Respectfully submitted,

William K. Konrad, Reg. No. 28,868
SPENSLEY HORN JUBAS & LUBITZ
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(310) 553-5050

Dated: May 16, 1994

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to Commissioner of Patents and Trademarks, Washington, D.C. 20231 on 5/16/94.

William K. Konrad, R.N. 28,868

(Signature)

5/16/94
Date